PRODUCT SPECIFICATION

Part No.:	**QSFP-100G-SR4**	
Description:	100G QSFP28 SR4 Transceiver, MMF 850nm 100m	
Release Date	**Rev.**	**Revision Change Description**
2018/07/07	A0	New Release
2020/12/28	A1	Template Update
2021/03/30	A2	BER Spec changed from BER<10^{-12} to BER<$5x10^{-5}$
2023/07/10	A3	Industrial Temperature added
2025/11/14	A4	1. Update the Package Outline

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Features

- ✧ Up to 25.78 Gbps Data rate per channel
- ✧ Maximum link length of 100m over OM3 MM fiber
- ✧ High Reliability 850nm VCSEL technology
- ✧ Electrically hot-pluggable
- ✧ MTP/MPO optical connector
- ✧ Operating case temperature range:
  Commerical : 0 to +70°C
  Industrial : -40 to +85°C
- ✧ RoHS-6 Compliant
- ✧ Power dissipation < 2.0 W

Application

- ✧ 100G Ethernet
- ✧ Data center

Standard

- ✧ IEEE 802.3bm
- ✧ QSFP28 MSA
- ✧ SFF-8636

Specification:

Absolute Maximum Ratings				
Parameter	**Symbol**	**Min**	**Max**	**Unit**
Storage Ambient Temperature	T_{STG}	-40	85	℃
Operating Humidity	H_O	5	95	%
Power Supply Voltage	Vcc	-0.3	4	V
Signal Input Voltage		Vcc-0.3	Vcc +0.3	V

Recommended Operating Conditions					
Parameter	**Symbol**	**Min**	**Typical**	**Max**	**Unit**
Operating Case Temperature	Tc	0		70	℃
Power Supply Voltage	Vcc	3.135	3.3	3.465	V
Power Supply Current	ICC			750	mA
Data Rate,each Lane			25.78125		Gbps
Fiber Length 50/125μm core OM3 MMF		-	100	-	m

Optical transmitter Characteristics						
Parameter	**Symbol**	**Min**	**Typical**	**Max**	**Unit**	**Notes**
Average Launch Power each lane	Pavg	-8.4		2.4	dBm	
Transmitter and Dispersion Penalty each lane	TDP			4.3	dB	
Wavelength Assignment	λ0	840	850	860	nm	
Spectral Width(-20dB)	Δλ			0.6	nm	
Extinction Ratio	ER	2			dB	
Optical Return Loss Tolerance	ORL			12	dBm	
Output Eye Diagram	Compliant with IEEE802.3bm eye mask					

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Optical receiver Characteristics							
Parameter		**Symbol**	**Min**	**Typical**	**Max**	**Unit**	**Notes**
Rx Sensitivity per lane		S			-9.5	dBm	1
Receiver Wavelength		λ_0	840	850	860	nm	
Optical Power Input Overload		P_{in-max}	2.4			dBm	
LOS	Optical De-assert	Pd			-12	dBm	
	Optical Assert	Pa	-30				
Receiver Reflectance		Rr			-12	dB	

Notes:

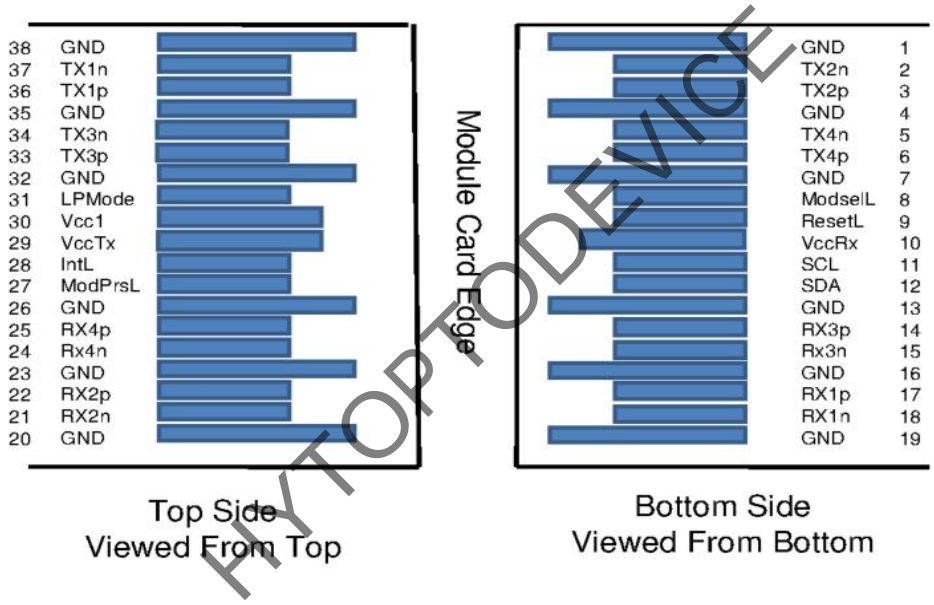
1. Measured with a PRBS 2^{31}-1 test pattern, @25.78Gb/s, BER<5X10-^{5}.

Electrical Characteristics						
Parameter	**Symbol**	**Min**	**Typical**	**Max**	**Unit**	**Notes**
Supply Voltage	Vcc	3.14	3.3	3.46	mV	
Supply Current	Icc			750	mV	
Input differential impedance	Rin		100		Ω	1
Differential data input swing	Vin,pp	180		1000	mV	
Single ended input voltage tolerance	VinT	-0.3		4.0	V	
Differential data output swing	Vout,pp	300		850	mV	2
Single ended output voltage		-0.3		4.0	V	

Notes:

1. Connected directly to TX data input pins. AC coupled thereafter.
2. Into 100Ω ohms differential termination.

Pin Definition



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Figure1 QSFP MSA-compliant 38-pin connector

Pin	Symbol	Name/Description	Notes
1	GND	Transmitter Ground (Common with Receiver Ground)	1
2	TX2N	Transmitter Inverted Data Input	
3	TX2P	Transmitter Non-Inverted Data Input	
4	GND	Ground	1
5	TX4N	Transmitter Inverted Data Input	
6	TX4P	Transmitter Non-Inverted Data Input	
7	GND	Ground	1
8	ModSelL	Module Select	
9	ResetL	Module Reset	
10	Vcc Rx	+3.3 V Power supply receiver	2

11	SCL	2-wire serial interface clock	
12	SDA	2-wire serial interface data	
13	GND	Ground	
14	RX3P	Receiver Non-Inverted Data Output	
15	RX3N	Receiver Inverted Data Output	
16	GND	Ground	1
17	RX1P	Receiver Non-Inverted Data Output	
18	RX1N	Receiver Inverted Data Output	
19	GND	Ground	1
20	GND	Ground	1
21	RX2N	Receiver Inverted Data Output	
22	RX2P	Receiver Non-Inverted Data Output	
23	GND	Ground	1
24	RX4N	Receiver Inverted Data Output	1
25	RX4P	Receiver Non-Inverted Data Output	
26	GND	Ground	1
27	ModPrsL	Module Present	
28	IntL	Interrupt	
29	Vcc Tx	+3.3 V Power supply transmitter	2
30	Vcc1	+3.3 V Power Supply	2
31	LPMode	Low Power Mode	
32	GND	Ground	1
33	TX3P	Transmitter Non-Inverted Data Input	
34	TX3N	Transmitter Inverted Data input	
35	GND	Ground	1
36	TX1P	Transmitter Non-Inverted Data Input	
37	TX1N	Transmitter Inverted Data input	
38	GND	Ground	1

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Table 1: QSFP28 Module PIN Definition

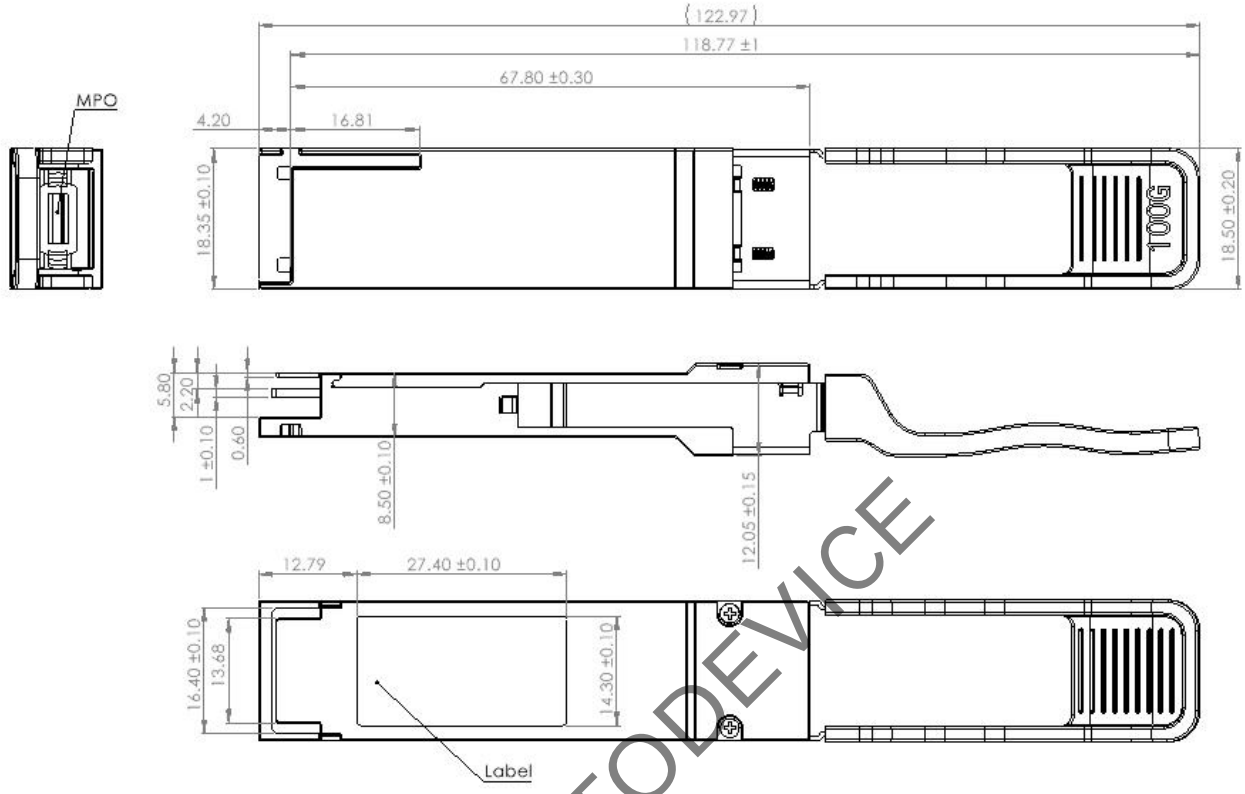
Notes：

1. GND is the symbol for signal and supply (power) common for QSFP28 modules. All are common within the QSFP28 module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.

2. VccRx, Vcc1 and VccTx are the receiving and transmission power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown in Figure 3 below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP28 transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.

Package Outline

Dimensions are in millimeters. All dimensions are ±0.2mm unless otherwise specified. (Unit: mm)



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Regulatory Compliance

Feature	Test	Method
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883E Method 3015.7	Class 1(>1000V for SFI pins, >2000Vfor other pins.)
Electrostatic Discharge (ESD) Immunity	IEC61000-4-2	Class 2(>4.0kV)
Electromagnetic Interference (EMI)	CISPR22 ITE Class B FCC Class B CENELEC EN55022 VCCI Class 1	Comply with standard
Immunity	IEC61000-4-3	Comply with standard
Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1,2	Compatible with Class I laser Product

Ordering Information

Part. No	Specifications								
	Pack	Rate (Gbps)	Tx (nm)	Po (dBm)	RX	Sen (dBm)	Temp (℃)	Reach (M)	DDM
QSFP-100G-SR4	QSFP28	100G	VCSEL	-8.4~2.4	PIN	<-9.5	0~70	100	Y
QSFP-100G-SR4-I	QSFP28	100G	VCSEL	-8.4~2.4	PIN	<-9.5	-40~85	100	Y

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